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2815
PATENT
AUS9 RE9999711-US2
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re Application of:
Hoorz Assaderaghi, et al.

Serial No.: 09/964,127

Group Art Unit: 2815

Filed: September 26, 2001

Examiner: Unknown

FOR: METHOD FOR PRODUCING
AN INTEGRATED CIRCUIT
CAPACITOR

Commissioner for Patents
Washington, D.C. 20231

#3
JP
4/4/02

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §1.97**

Dear Sir:

The following items are enclosed for filing in the above-identified patent application:

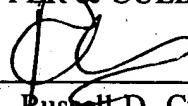
- Form PTO/SB/08A, along with copies of listed references
 Stamped, self-addressed postcard to be returned

If there is any fee due please charge such fee to Deposit Account No. 19-1453 (Our File No. AUS9-1999-711-US2).

Respectfully submitted,

SHAFFER & CULBERTSON, L.L.P.

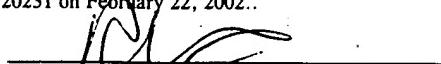
Date: February 22, 2002.

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231 on February 22, 2002..


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